



Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet **1** of **14****Complete if Known**

Application Number	<b>10/037,593</b>
Filing Date	<b>October 19, 2001</b>
First Named Inventor	<b>Matthew P. Kulig</b>
Art Unit	<b>2157</b>
Examiner Name	<b>Barbara N. Burgess</b>
Attorney Docket Number	<b>60022210-0157</b>

**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
	AA	US 7,117,370	10-03-2006	Khan et al.	
	AB	US 7,103,799	09-05-2006	Dixon	
	AC	US 7,093,023	08-15-2006	Lockwood et al.	
	AD	US 6,981,054	12-27-2005	Krishna	
	AE	US 6,944,168	09-13-2005	Paatela et al.	
	AF	US 6,886,103	04-26-2005	Brustoloni et al.	
	AG	US 6,856,981	02-15-2005	Wyschogrod et al.	
	AH	US 6,807,156	10-19-2004	Veres et al.	
	AI	US 6,772,345	08-03-2004	Shetty	
	AJ	US 6,765,918	07-20-2004	Dixon et al.	
	AK	US 6,711,558	03-23-2004	Indeck et al.	
	AL	US 6,578,147	06-10-2003	Shanklin et al.	
	AM	US 6,463,474	10-08-2002	Fuh et al.	
	AN	US 6,430,272	08-06-2002	Maruyama et al.	
	AO	US 6,412,000	06-25-2002	Riddle et al.	
	AP	US 6,397,335	05-28-2002	Franczek et al.	
	AQ	US 6,397,259	05-28-2002	Lincke et al.	
	AR	US 6,363,384	03-26-2002	Cookmeyer, II et al.	
	AS	US 6,317,795	11-13-2001	Malkin et al.	
	AT	US 6,336,150	01-01-2002	Ellis et al.	
	AU	US 6,317,795	11-13-2001	Malkin et al.	
	AV	US 6,279,113	08-21-2001	Vaidya	
	AW	US 6,263,321	07-17-2001	Daughtery	
	AX	US 6,237,028	05-22-2001	Jackson	
	AY	US 6,233,684	05-15-2001	Stefik et al.	
	AZ	US 6,233,618	05-15-2001	Shannon	
	BA	US 6,226,676	05-01-2001	Crump et al.	

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				Filing Date	October 19, 2001
				First Named Inventor	Matthew P. Kulig
				Art Unit	2157
				Examiner Name	Barbara N. Burgess
Sheet	2	of	14	Attorney Docket Number	60022210-0157

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
	BB	US 6,219,786	04-17-2001	Cunningham et al.	
	BC	US 6,216,173	04-10-2001	Jones et al.	
	BD	US 6,205,148	03-20-2001	Takahashi et al.	
	BE	US 6,173,276	01-09-2001	Kant et al.	
	BF	US 6,169,969	01-02-2001	Cohen	
	BG	US 6,147,976	11-14-2000	Shand et al.	
	BH	US 6,138,176	10-24-2000	McDonald et al.	
	BI	US 6,134,551	10-17-2000	Aucsmith	
	BJ	US 6,115,751	09-05-2000	Tam et al.	
	BK	US 6,112,181	08-29-2000	Shear et al.	
	BL	US 6,108,782	08-22-2000	Fletcher et al.	
	BM	US 6,105,067	08-15-2000	Batra	
	BN	US 6,073,160	06-06-2000	Grantham et al.	
	BO	US 6,070,172	05-30-2000	Lowe	
	BP	US 6,067,569	05-23-2000	Khaki et al.	
	BQ	US 6,061,662	05-09-2000	Makivic	
	BR	US 6,044,407	03-28-2000	Jones et al.	
	BS	US 6,023,760	02-08-2000	Kartunnen	
	BT	US 5,991,881	11-23-1999	Conklin et al.	
	BU	US 5,974,414	10-26-1999	Stanczak et al.	
	BV	US 5,943,429	08-24-1999	Handal	
	BW	US 5,930,753	07-27-1999	Potamianos et al.	
	BX	US 5,913,211	06-15-1999	Nitta	
	BY	US 5,884,286	03-16-1999	Daughtery, III	
	BZ	US 5,864,738	01-26-1999	Kessler et al.	
	CA	US 5,826,075	10-20-1998	Bealkowski et al.	

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				Art Unit	2157
Examiner Name	Barbara N. Burgess				
Attorney Docket Number	60022210-0157				
Sheet	3	of	14		
CB	US 5,819,290	10-06-1998	Fujita		
CC	US 5,818,273	10-06-1998	Vora et al.		
CD	US 5,813,000	09-22-1998	Furlani		
CE	US 5,805,832	09-08-1998	Brown et al.		
CF	US 5,781,921	07-14-1998	Nichols		
CG	US 5,781,772	07-14-1998	Wilkinson III		
CH	US 5,774,839	06-30-1998	Shlomot		
CI	US 5,774,835	06-30-1998	Ozawa		
CJ	US 5,740,466	04-14-1998	Geldman et al.		
CK	US 5,721,898	02-24-1998	Beardsley et al.		
CL	US 5,701,464	12-23-1997	Aucsmith		
CM	US 5,651,125	07-22-1997	Witt et al.		
CN	US 5,629,980	05-13-1997	Stefik et al.		
CO	US 5,546,578	08-13-1996	Takada		
CP	US 5,544,352	08-06-1996	Egger		
CQ	US 5,497,488	03-05-1996	Akizawa et al.		
CR	US 5,488,725	01-30-1996	Turtle et al.		
CS	US 5,481,735	01-02-1996	Mortensen et al.		
CT	US 5,465,353	11-07-1995	Hull et al.		
CU	US 5,461,712	10-24-1995	Chelstowski et al.		
CV	US 5,432,822	07-11-1995	Kaewell, Jr.		
CW	US 5,418,951	05-23-1995	Damashek		
CX	US 5,396,253	03-07-1995	Chia		
CY	US 5,388,259	02-07-1995	Fleischman et al.		
CZ	US 5,339,411	08-16-1994	Heaton Jr. et al.		
DA	US 5,327,521	07-05-1994	Savic et al.		
DB	US 5,319,776	06-07-1994	Hile et al.		

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	DC	US 5,265,065	11-23-1993	Turtle	
	DD	US 5,255,136	10-19-1993	Machado et al.	
	DE	US 5,249,292	09-28-1993	Chiappa	
	DF	US 5,243,655	09-07-1993	Wang	
	DG	US 5,226,165	07-06-1993	Martin	
	DH	US 5,179,626	01-12-1993	Thomson	
	DI	US 5,163,131	11-10-1992	Row et al.	
	DJ	US 5,140,692	08-18-1992	Morita	
	DK	US 5,101,424	03-31-1992	Clayton et al.	
	DL	US 5,050,075	09-17-1991	Herman et al.	
	DM	US 5,023,910	06-11-1991	Thomson	
	DN	US 4,941,178	07-10-1990	Chuang	
	DO	US 4,823,306	04-18-1989	Barbic et al.	
	DP	US 4,550,436	10-29-1985	Freeman et al.	
	DQ	US 4,464,718	08-07-1984	Dixon et al.	
	DR	US 4,385,393	05-24-1983	Chaure et al.	
	DS	US 4,314,356	02-02-1982	Scarborough	
	DT	US 4,298,898	11-03-1981	Cardot	
	DU	US 4,081,607	03-28-1978	Vitols	
	DV	US 3,906,455	09-16-1975	Houston et al.	
	DW	US 3,848,235	11-12-1974	Lewis et al.	
	DX	US 3,824,375	07-16-1974	Gross et al.	
	DY	US 3,729,712	04-24-1973	Glassman	

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	DZ	US 3,906,455	09-16-1975	Houston et al.	
	EA	US 3,848,235	11-12-1974	Lewis et al.	
	EB	US 3,824,375	07-16-1974	Gross et al.	
	EC	US 3,729,712	04-24-1973	Glassman	
	ED	US 3,611,314	10-05-1971	Pritchard, Jr. et al.	
	EE	US 3,601,808	08-24-1971	Vlack	
	EF	US 2002/0882967 A1	06-27-2002	Kaminsky et al.	
	EG	US 2002/162025 A1	08-08-2002	Pruthi et al.	
	EH	US 2002/0129140 A1	09-12-2002	Peled et al.	
	EI	US 2002/0105911 A1	08-08-2002	Pruthi et al.	
	EJ	US 2003/0023876 A1	01-30-2003	Bardsley et al.	
	EK	US 2003/0043805 A1	03-06-2003	Graham et al.	
	EL	US 2003/0055658 A1	03-20-2003	RuDusky	
	EM	US 2003/0055770 A1	03-20-2003	RuDusky	
	EN	US 2003/0055771 A1	03-20-2003	RuDusky	
	EO	US 2003/0065943 A1	04-03-2003	Geis et al.	
	EP	US 2003/0093347 A1	05-15-2003	Gray	
	EQ	US 2003/0110229 A1	06-12-2003	Kulig et al.	
	ER	US 2003/0126065 A1	07-03-2003	Eng et al.	
	ES	US 2003/0177253 A1	09-18-2003	Schuehler et al.	
	ET	US 2004/0028047 A1	02-12-2004	Hou et al.	
	EU	US 2004/0034587 A1	02-19-2004	Amberson et al.	
	EV	US 2004/0049596 A1	03-11-2004	Schuehler et al.	
	EW	US 2004/0177340 A1	09-09-2004	Hsu et al.	

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	EX	US 2004/0186804 A1	09-23-2004	Chakraborty et al.	
	EY	US 2004/0186814 A1	09-23-2004	Chalermkraivuth et al.	
	EZ	US 2004/0199448 A1	10-07-2004	Chalermkraivuth et al.	
	FA	US 2004/0205149 A1	10-14-2004	Dillon et al.	
	FB	US 2005/033672 A1	02-10-2005	Lasry et al.	
	FC	US 2005/0091142 A1	04-28-2005	Renton et al.	
	FD	US 2005/0097027 A1	05-05-2005	Kavanagh	
	FE	US 2005/0131790 A1	06-16-2005	Benzschawel et al.	
	FF	US 2005/0187844 A1	08-25-2005	Chalermkraivuth et al.	
	FG	US 2005/0187845 A1	08-25-2005	Elkund et al.	
	FH	US 2005/0187846 A1	08-25-2005	Subbu et al.	
	FI	US 2005/0187847 A1	08-25-2005	Bonissone et al.	
	FJ	US 2005/0187848 A1	08-25-2005	Bonissone et al.	
	FK	US 2005/0187849 A1	08-25-2005	Bollapragada et al.	
	FL	US 2005/0197938 A1	09-08-2005	Davie et al.	
	FM	US 2005/0197939 A1	09-08-2005	Davie et al.	
	FN	US 2005/0197948 A1	09-08-2005	Davie et al.	
	FO	US 2005/0216384 A1	09-29-2005	Partlow et al.	
	FP	US 2005/0267836 A1	09-01-2005	Crosthwaite et al.	
	FQ	US 2005/0283423 A1	09-22-2005	Moser et al.	
	FR	US 2006/0020536 A1	01-26-2006	Renton et al.	
	FS	US 2006/0031154 A1	02-09-2006	Noviello et al.	
	FT	US 2006/0031156 A1	02-09-2006	Noviello et al.	
	FU	US 2006/0059064 A1	03-16-2006	Glinberg et al.	
	FV	US 2006/0059065 A1	03-16-2006	Glinberg et al.	

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Examiner Name	Barbara N. Burgess				
Attorney Docket Number	60022210-0157				
Sheet	7	of	14		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
	FW	US 2006/0059066 A1	03-16-2006	Glinberg et al.	
	FX	US 2006/0059067 A1	03-16-2006	Glinberg et al.	
	FY	US 2006/0059068 A1	03-16-2006	Glinberg et al.	
	FZ	US 2006/0059069 A1	03-16-2006	Glinberg et al.	
	GA	US 2006/0059083 A1	03-16-2006	Friesen et al.	
	GB	US 2006/0059099 A1	03-16-2006	Ronning et al.	
	GC	US RE 36946	11-07-2000	Diffie et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
	GD	EP 0851358	03-05-2003	Sun Microsystems		
	GE	EP 0880088	11-25-1998	Mitsubishi Corp.		
	GF	EP 0887723	12-30-1998	IBM Corp.		
	GG	EP 0911738 A2	04-28-1999	Calluna Tech Ltd.		
	GH	WO 01/022425 A1	03-29-2001	Seagate Technology		
	GI	WO 01/80082 A2	10-25-2001	Washington University		
	GJ	WO 01/80558 A2	10-25-2001	Solidstreaming, Inc.		
	GK	WO 02/61525 A3	08-08-2002	Pirus Networks		
	GL	WO 04/17604 A2	02-26-2004	Washington University		
	GM	WO 05/017708 A2	02-24-2005	Washington University		

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	GN	AGERE SYSTEMS, "Lucent Delivers 'Payload Plus' Network Processors for Programmable, Multiprotocol, OC-48c Processing", Agere Systems Press Release, October 30, 2000, Allentown, PA.			
	GO	AGERE SYSTEMS, "Payload Plus™ Agere System Interface", Agere Systems Product Brief, June 2001, downloaded from Internet, January 2002, Allentown, PA.			
	GP	ANEROUSIS, Nikos et al., "Using the AT&T Labs PacketScope for Internet Measurement, Design, and Performance Analysis", AT&T Services and Infrastructure Performance Symposium, AT&T Labs-Research, Florham, Park, NJ, October 1997, <a href="http://www.net.t-labs.tu-berlin.de/papers/ACDFGKMRR-UPSIMDP-97.pdf">http://www.net.t-labs.tu-berlin.de/papers/ACDFGKMRR-UPSIMDP-97.pdf</a>			
	GQ	ARTAN, N. Sertec et al., "Multi-packet Signature Detection using Prefix Bloom Filters", IEEE Globecom 2005, pages 1811-1816.			
	GR	BABOESCU, Florin et al., "Scalable Packet Classification", SIGCOMM'01, pp. 199-210, San Diego, CA, Aug. 27-21, 2001; <a href="http://www.ecse.rpi.edu/homepages/shivkuma/teaching/sp2001/readings/baboescu-pkt-classification.pdf">http://www.ecse.rpi.edu/homepages/shivkuma/teaching/sp2001/readings/baboescu-pkt-classification.pdf</a>			
	GS	BAEZA-YATES and NAVARRO, "New and Faster Filters for Multiple Approximate String Matching", Random Structures and Algorithms (RSA), Vol. 20, No. 1, January 2002, pp. 23-49.			
	GT	BERK, Elliot, "JLex: A lexical analyzer generator for Java™", Sept. 6, 2000, downloaded from <a href="http://www.cs.princeton.edu/~appel/modern/java/Jlex/">http://www.cs.princeton.edu/~appel/modern/java/Jlex/</a> in January 2002			
	GU	BLOOM, Burton H., "Space/Time Trade-offs in Hash Coding With Allowable Errors", Communications of the ACM, 13(7): 422-426, Computer Usage Company, Newton Upper Falls, Massachusetts, USA, July 1970.			
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	GV	BRAUN, Florian et al., "Layered Protocol Wrappers for Internet Packet Processing in Reconfigurable Hardware", Proceedings of Hot Interconnects 9 (HotI-9) Stanford, CA, August 22-24, 2001, pp. 93-98.			
	GW	CHOI, Sumi et al., "Design of a Flexible Open Platform for High Performance Active Networks", Allerton Conference, Champaign, IL, 1999.			
	GX	CLOUTIER, Jocelyn et al., "VIP: An FPGA-Based Processor for Image Processing and Neural Networks", Proceedings of Fifth International Conference on Microelectronics for Neural Networks, February 12, 1996, pp. 330-336, Los Alamitos, California			
	GY	COMPTON, Katherine et al., "Reconfigurable Computing: A Survey of Systems and Software", Technical Report, Northwestern University, IL, 16, Dept. of ECE, 1999.			
	GZ	CUPPU, Vinodh and JACOB, Bruce, "Organizational Design Trade-offs at the DRAM, Memory Bus, and Memory Controller Level: Initial Results," Technical Report UMB-SCA-TR-1999-2, Univ. of Maryland Systems & Computer Architecture Group, pp. 1 - 10, Nov. 1999, College Park, MD.			
	HA	DHARMAPURIKAR, Sarang et al., "Longest Prefix Matching Using Bloom Filters," SIGCOMM 2003, pp. 201 -212, Karlsruhe, Germany, August 25-29, 2003.			
	HB	DHARMAPURIKAR, Sarang et al., "Robust TCP Stream Reassembly In the Presence of Adversaries", Proceedings of the 14th Conference on USENIX Security Symposium - Volume 14, 16 pages, Baltimore, MD, 2005. <a href="http://www.icir.org/vern/papers/TcpReassembly/TcpReassembly.pdf">http://www.icir.org/vern/papers/TcpReassembly/TcpReassembly.pdf</a>			
	HC	FRANKLIN, R. et al., "Assisting Network Intrusion Detection with Reconfigurable Hardware", Symposium on Field-Programmable Custom Computing Machines (FCCM 2002), April 2002, Napa, California			
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	HD	FU, Henry et al., "The FPX KCPSM Module: An Embedded, Reconfigurable Active Processing Module for the Field Programmable Port Extender (FPX)", Washington University, Department of Computer Science, Technical Report WUCS-01-74, July 26, 2001, St. Louis, MO.			
	HE	GUPTA, Pankaj and McKEOWN, Nick, "Packet Classification on Multiple Fields", Computer Systems Laboratory, Stanford University, Stanford, CA, <a href="http://yuba.stanford.edu/~pankaj/paps/sig99.pdf">http://yuba.stanford.edu/~pankaj/paps/sig99.pdf</a> , and Computer Communication Review, a publication of ACM SIGCOMM, volume 29, number 4, October 1999.			
	HF	GURTOV, Andrei, "Effect of Delays on TCP Performance", Cellular Systems Development, Sonera Corporation, online at <a href="http://www.cs.helsinki.fi/u/gurtov/papers/pwc01.pdf">http://www.cs.helsinki.fi/u/gurtov/papers/pwc01.pdf</a>			
	HG	HAUCK, Scott and AGARWAL, Anant, "Software Technologies for Reconfigurable Systems", Northwestern University, IL, Dept. of ECE, Technical Report, pp. 1-40, 1996.			
	HH	HOLLAAR, Lee A., "Hardware Systems for Text Information Retrieval", Proceedings of the Sixth Annual International ACM Sigir Conference on Research and Development in Information Retrieval, June 6-8, 1983, pp. 3-9, Baltimore, Maryland, USA			
	HI	JACOBSON, V. et al., "RFC 1072: TCP Extensions for Long-Delay Paths", Network Working Group, October 1988, online at <a href="http://www.faqs.org/ftp/rfc/pdf/rfc1072.txt.pdf">http://www.faqs.org/ftp/rfc/pdf/rfc1072.txt.pdf</a>			
	HJ	JACOBSON, Van et al., "tcpdump - dump traffic on a network" June 30, 1997, online at <a href="http://www.cse.cuhk.edu.hk/~cslui/CEG4430/tcpdump.ps.gz">www.cse.cuhk.edu.hk/~cslui/CEG4430/tcpdump.ps.gz</a>			
	HK	KEUTZER, Kurt & SHAH, Niraj, "A Survey of Programmable Platforms - Network Proc", University of California-Berkeley, CA, September 2001, Berkeley, CA.			
	HL	LOCKWOOD, John & LIM, David, "Hello, World: A Simple Application for the Field-programmable Port Extender (FPX)", Washington University, Department of Computer Science, Technical Report WUCS-TM-00-12, July 11, 2000, St. Louis, MO.			
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	HM	LOCKWOOD, John W., "Simulation of the Hello World Application for the Field-programmable Port Extender (FPX)", Washington University, Applied Research Lab, Spring 2001 Gigabits Kits Workshop, St. Louis, MO.			
	HN	LOCKWOOD, John, "Field-programmable Port Extender (FPX)", January 2002 Workshop, Washington University, St. Louis, MO, January 3-4, 2002. (slides and handouts from workshop).			
	HO	LOCKWOOD, John W. et al., "Field Programmable Port Extender (FPX) for Distributed Routing and Queuing", ACM International Symposium on Field Programmable Gate Arrays (FPGA '2000), Monterey, CA, February 2000, pp.137-144.			
	HP	LOCKWOOD, John et al., "Parallel FPGA Programming over Backplane Chassis", Washington University, Department of Computer Science, Technical Report WUCS-OO-I I, June 12, 2000, St. Louis, MO.			
	HQ	LOCKWOOD, John W. et al., "Reprogrammable Network Packet Processing on the Field Programmable Port Extender (FPX)", ACM International Symposium on Field Programmable Gate Arrays (FPGA 2001), Monterey, CA, February 11-13, 2001, pp. 87-93.			
	HR	LOCKWOOD, John W., "An Open Platform for Development of Network Processing Modules in Reprogrammable Hardware", IEC Designcon 2001, Santa Clara, CA, January 2001, Paper WB-19			
	HS	LOCKWOOD, John W., "Evolvable Internet Hardware Platforms", NASA DoD Workshop on Evolvable Hardware (EHWOI), Long Beach, CA, July 12-14, 2001, pp. 271-279.			
	HT	LOCKWOOD, John W., "Platform and Methodology for Teaching Design of Hardware Modules in Internet Routers and Firewalls", IEEE Computer Society International Conference on Microelectronic Systems Education (MSE 2001), Las Vegas, NV, June 17-18, 2001, pp. 56-57.			
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	HU	LUCENT, "Lucent Technologies delivers PayloadPlus network processors for programmable, multi-protocol, OC-48c processing", Lucent Technologies Press Release, downloaded from <a href="http://www.lucent.com/press/1000/0010320.meb.html">http://www.lucent.com/press/1000/0010320.meb.html</a> on March 21, 2002.			
	HV	MOSCOLA, James M. et al., "FPGrep and FPSed: Regular Expression Search and Substitution for Packet Streaming in Field Programmable Hardware", Dept. of Computer Science, Applied Research Lab, Washington University, St. Louis, MO, January 8, 2002.			
	HW	MOSCOLA, James, "FPgrep and FPSed: Packet Payload Processors for Managing the Flow of Digital Content on Local Area Networks and the Internet", Masters Thesis, Sever Institute of Technology, Washington University, St. Louis, Missouri, August 2003.			
	HX	MOSCOLA, James et al. "FPSed: A Streaming Content Search-and-Replace Module for an Internet Firewall", Proceedings of Hot Interconnects, 11th Symposium on High Performance Interconnects, pp. 122-129, Aug. 20, 2003.			
	HY	NAVARRO, Gonzalo, "A Guided Tour to Approximate String Matching", ACM Computing Surveys, Vol. 33, No. 1, March 2001, pp. 31-88, and online at <a href="http://security.riit.tsinghua.edu.cn/seminar/2006_3_9/2001-A_Guided_Tour_to_Approximate_String_Matching.pdf">http://security.riit.tsinghua.edu.cn/seminar/2006_3_9/2001-A_Guided_Tour_to_Approximate_String_Matching.pdf</a> .			
	HZ	NECKER, Marc et al., "TCP-Stream Reassembly and State Tracking in Hardware" School of Electrical and Computer Engineering, Georgia Institute of Technology, Atlanta, GA, Proceedings of the 10th Annual IEEE Symposium on Field-Programmable Custom Computing Machines 2002, and online at: <a href="http://csdl2.computer.org/comp/proceedings/fccm/2002/1801/00/18010286.pdf">http://csdl2.computer.org/comp/proceedings/fccm/2002/1801/00/18010286.pdf</a>			
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	IA	PRAKASH, Amit et al., "OC-3072 Packet Classification Using BDDs and Pipelined SRAMs", Department of Electrical and Computer Engineering, The University of Texas at Austin, and online at <a href="http://users.ece.utexas.edu/~adnan/publications/prakash-hoti-01.pdf">http://users.ece.utexas.edu/~adnan/publications/prakash-hoti-01.pdf</a>			
	IB	PRAMANIK, Sakti et al., "A Hardware Pattern Matching Algorithm on a Dataflow", The Computer Journal, July 1, 1985, pp. 264-269, Vol. 28, No. 3, Oxford University Press, London, Great Britain			
	IC	RFC793: Transmission Control Protocol, DARPA Internet Program, Protocol Specification, September 1981, online at <a href="http://www.faqs.org/ftp/rfc/pdf/rfc793.txt.pdf">http://www.faqs.org/ftp/rfc/pdf/rfc793.txt.pdf</a>			
	ID	SCHUEHLER, David V. et al., "Architecture for a Hardware Based, TCP/IP Content Scanning System", IEEE Micro, 24(1):62-69, Jan.-Feb. 2004, U.S.A.			
	IE	SCHUEHLER, David V. et al., "TCP-Splitter: A TCP/IP Flow Monitor in Reconfigurable Hardware", Hot Interconnects 10 (HotI-10), Stanford, CA, Aug. 21-23, 2002, pp. 127-131.			
	IF	SHAH, Niraj, "Understanding Network Processors", Version 1.0, University of California-Berkeley, September 4, 2001, Berkeley, CA.			
	IG	SIDHU, Reetinder et al., "Fast Regular Expression Matching using FPGAs", IEEE Symposium on Field Programmable Custom Computing Machines (FCCM 2001), April 2001			
	IH	SIDHU, Reetinder et al., "String Matching on Multicontext FPGAs using Self-Reconfiguration", FPGA '99: Proceedings of the 1999 ACMISIGDA 7th International Symposium on Field Programmable Gate Arrays, February 1999, pp. 217-226, Monterey, CA.			
	II	TAYLOR, David E. et al., "Dynamic Hardware Plugins (DHP): Exploiting Reconfigurable Hardware for High-Performance Programmable Routers", Computer Networks, 38(3): 295-310(16), 21 February 2002, and online at <a href="http://www.cc.gatech.edu/classes/AY2007/cs8803hpc_fall/papers/dhplugins.pdf">http://www.cc.gatech.edu/classes/AY2007/cs8803hpc_fall/papers/dhplugins.pdf</a>			
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				Application Number	<b>10/037,593</b>
				Filing Date	<b>October 19, 2001</b>
				First Named Inventor	<b>Matthew P. Kulig</b>
				Group Art Unit	<b>2157</b>
				Examiner Name	<b>Barbara N. Burgess</b>
Sheet	<b>14</b>	of	<b>14</b>	Attorney Docket No.	<b>60022210-0157</b>
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	IJ	TAYLOR, David E. et al., "Generalized RAD Module Interface Specification of the Field Programmable Port Extender (FPX) Version 2.0", Washington University, Department of Computer Science, Technical Report, WUCS-TM-01-15, July 5, 2001, St. Louis, MO.			
	IK	YAMAGUCHI et al., High Speed Homology Search with FPGAs, Proceedings of the Pacific Symposium on Biocomputing, Lihue, Hawaii, 7:271-282, January 3, 2002, and online at: <a href="http://psb.stanford.edu/psb-online/proceedings/psb02/yamaguchi.pdf">http://psb.stanford.edu/psb-online/proceedings/psb02/yamaguchi.pdf</a>			
	IL	YAN, Jeff et al., "Enhancing Collaborative Spam Detection with Bloom Filters", Proceedings of the 22nd Annual Computer Security Applications Conference (ACSAC '06), IEEE, pages 414-425, 2006.			
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